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## IN THE UNITED STATES PATENT & TRADEMARK OFFICE

Application No.:

09/966,391

Applicant:

DEMONE, Paul W

Filed:

September 28, 2001

TC/A.U.:

2816

Examiner:

NGUYEN, Minh T.

Docket No.:

16319-178US

Customer No.:

020988

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R., §§ 1.97-1.99, applicant submits the following information which may be of interest to the examiner in charge of the above referenced application for patent. A copy of the reference listed on the attached Form PTO-1449, List of Prior Art Cited by Applicant, is attached.

Respectfully submitted,

Kevin Pilla Registration No. 41,559

Date: November 20, 2003

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PTO/SB/08B (04-03)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	09/966,391	
				Filing Date	September 28, 2001	
				First Named Inventor	DEMONE, Paul W.	
				Art Unit ,	2816	
(Use at many sheets as necessary)			<del>¢¢¢ss</del> Bry)	Examiner Name	NGUYEN, Minh T.	
Sheet	1	of	1	Attorney Docket Number	16319-178US	

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Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, senal, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.  WITTERS, Johan S. et al., "Analysis and Modeling of On-Chip High-Voltage Generator Circuits for Use in EEPROM				
		Circuits", IEEE Journal of Solid-State Circuits, US, IEEE Inc., New York, Vol. 24, No. 5, October 1989, Pages 1372-1380				
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